ECN#

412.1

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Anidialit Engineering Change note:



ECN#:

T#: 412

			2011			
ASSEMBLY: B	3MW4011E	DESCRIPTION:	MW4-PXY CPU CARD			
New Assembly:		DATE:	25/09/01			
Current Assy Rev: T		Current Schematic Rev:	5.13			
New Assy Rev: U	J	New Schematic Rev:	5.14			
THIS ECN REPLACES ECN 412. TWO 2-PIN HEADERS REPLACED WITH SOLDER BLOBS.						
CMS BOMs Updated	C Excel BOMs Updated C	Subcontractor Records Upda	ted Compatibility Maintained:	Yes		
IMPLEMENTATION INFORMATION:						
Safety Issue	UL Compliance Issue	O New Feature	O Cost Reduction			
O Bug Fix	○ EMC Compliance Issue	O Quality Issue				
Reliability Reasons	O Software Change	O Cosmetic Change				
REASON FOR CHAI	NGE:					
	are crashing during JTAG scan.	om the ITAG chain				

DETAILS OF CHANGE:

- 1. Place a solder blob across pins T3 & T4 of U70. Fit the header to the bottom side of the board, laying flat.
- 2. Place a solder blob across pins T3 & T4 of U71. Fit the header to the bottom side of the board, laying flat.
- 3. Fit two shorting plugs (JMJ6765) to JP9 and JP10. NOTE: These two Jumpers and Solder Blobs must be removed in order to run the production JTAG-pod tests.
- 5. Place revision resistors (all 10k) as follows:

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		10k	omit
	R127	X	
	R128		X
	R133		X
	R134	X	
	R135		X
	R137	X	
	R136		X
	D120	\mathbf{v}	

6. Label the board with the "U" revision label so that the whole part number reads "BMW4011E-U"

NOTE TO SUBCONTRACTORS: This ECN applies to all future production and to all boards in WIP that exhibit a crash during JTAG scan or when "Determining System Hardware Configuration".

NOTE TO PRODUCTION: This ECN applies to all future production and to all boards in WIP that exhibit a crash during JTAG scan or when "Determining System Hardware Configuration".

NOTE TO FIELD SERVICE: No action required.

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